

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Nicolae Marin and Sridhar Kotikalapoodi  
Title: Band-Gap Reference Circuit With High Power Supply Ripple Rejection Ratio  
Application No.: Unknown  
Filing Date: Herewith  
Examiner: Unknown  
Group Art Unit: Unknown  
Confirmation No.: Unknown  
Law Office: Sidley Austin Brown & Wood LLP  
Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants wish to call the following documents to the attention of the Examiner for the above-identified patent application. This application has a filing date after June 30, 2003. Therefore, copies of the U.S. patents and U.S. patent application publications listed on the accompanied form PTO-1449 are not enclosed.

A form PTO-1449 listing these documents is enclosed.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made; or

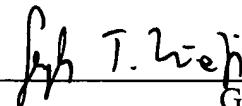
3. an admission that the information listed herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL LABEL NO.:

Respectfully submitted,

EV 305 257 426 US

By:



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November 19, 2003  
SIDLEY AUSTIN BROWN & WOOD LLP  
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U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.	Application No.		
				24317/82751	Unknown		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Applicants: Nicolae Marin, et al.			
				Filing Date		Group	
				Herewith		Unknown	
<b>U.S. Patent Documents</b>							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	1.	6,570,436	May 27, 2003	Kronmueller			
	2.	6,549,065	Apr. 15, 2003	Opris			
	3.	6,294,902	Sep. 25, 2001	Moreland			
	4.	6,281,743 B1	Aug. 28, 2001	Doyle			
	5.	6,188,270 B1	Feb. 13, 2001	Boerstler			
	6.	6,181,121 B1	Jan. 30, 2001	Kirkland, et al.			
<b>Foreign Patent Documents</b>							
							Translation
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	7.	A. Paul Brokaw, "A Simple Three-Terminal IC Bandgap Reference," <i>IEEE Journal of Solid-State Circuits</i> , Vol. sc-9, No. 6, (December 1994), pages 388-393.					
	8.	Robert J. Widlar, "New Developments in IC Voltage Regulators," <i>IEEE Journal of Solid-State Circuits</i> , Vol. sc-6, No. 1, (February 1971), pages 2-7.					
Examiner:			Date Considered:				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.							

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